DORNPACKAGE

- Low Input Bias Current . . . 50 pA Typ
- Low Input Noise Current 0.01 pA/√Hz Typ
- Low Total Harmonic Distortion
- Low Supply Current . . . 8 mA Typ
- Gain Bandwidth . . . 3 MHz Typ
- High Slew Rate . . . 13 V/μs Typ
- Pin Compatible With the LM348

(TOP VIEW) **10UT** 14∏ 40UT 1IN - 2 13 4IN-1IN+∏ 3 Π 4IN + 12 V_{CC+} 4 IJ∨_{CC} – 2IN+[] 5 10 ¶ 3IN+ 2IN-∏ 6 9∏3IN-8 🛮 30UT 20UT 🛛

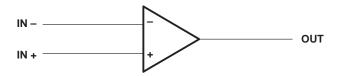
description

These devices are low-cost, high-speed, JFET-input operational amplifiers. They require low supply current yet maintain a large gain-bandwidth product and a fast slew rate. In addition, their matched high-voltage JFET inputs provide very low input bias and offset current.

The LF347 and LF347B can be used in applications such as high-speed integrators, digital-to-analog converters, sample-and-hold circuits, and many other circuits.

The LF347 and LF347B are characterized for operation from 0°C to 70°C.

symbol (each amplifier)



AVAILABLE OPTIONS

	Viemov	PACKAGE				
T _A V _{IO} max AT 25°C		SMALL OUTLINE (D)	PLASTIC DIP (N)			
0°C to 70°C	10 mV	LF347D	LF347N			
0010700	5 mV	LF347BD	LF347BN			

The D packages are available taped and reeled. Add R suffix to the device type (e.g., LF347DR).

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, V _{CC +}	18 V
Supply voltage V _{CC}	18 V
Differential input voltage, V _{ID}	±30 V
Input voltage, V _I (see Note 1)	±15 V
Duration of output short circuit	unlimited
Continuous total power dissipation	See Dissipation Rating Table
Operating temperature range	0°C to 70°C
Storage temperature range	65°C to 150°C
Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds	260°C

NOTE 1: Unless otherwise specified, the absolute maximum negative input voltage is equal to the negative power supply voltage.



DISSIPATION RATING TABLE

PACKAGE	$T_{\mbox{\scriptsize A}} \leq 25^{\circ}\mbox{\scriptsize C}$ POWER RATING	DERATING FACTOR	DERATE ABOVE T _A	T _A = 70°C POWER RATING
D	608 mW	7.6 mW/°C	61°C	608 mW
N	680 mW	N/A	N/A	680 mW

recommended operating conditions

	MIN	MAX	UNIT
Supply voltage, V _{CC +}	3.5	18	V
Supply voltage, V _{CC} _	-3.5	-18	V

electrical characteristics over operating free-air temperature range, $V_{\text{CC}\pm}$ = ± 15 V (unless otherwise specified)

PARAMETER		T _A †		LF347		LF347B			UNIT
			MIN	TYP	MAX	MIN	TYP	MAX	UNII
Input offset voltage	$V_{IC} = 0,$ R _S = 10 k Ω	25°C		5	10		3	5	mV
input onset voltage		Full range			13			7	IIIV
Average temperature coefficient of input offset voltage	$V_{IC} = 0,$ R _S = 10 k Ω			18			18		μV/°C
hand effect summed.	., .	25°C		25	100		25	100	pА
input offset current+	AIC = 0	70°C			4			4	nA
hand black some of t	V:0 - 0	25°C		50	200		50	200	pА
Input bias current+	AIC = 0	70°C			8			8	nA
Common-mode input voltage range			±11	-12 to		±11	-12 to		V
Maximum peak output voltage swing	R _L = 10 kΩ		±12	±13.5		±12	±13.5		V
l anno signal differential college	$V_O = \pm 10 \text{ V},$ $R_L = 2 \text{ k}\Omega$	25°C	25	100		50	100		V/mV
Large-signal differential voltage		Full range	15			25			
Input resistance	T _A = 25°C			1012			1012		Ω
Common-mode rejection ratio	$R_S \le 2 k\Omega$		70	100		80	100		dB
Supply-voltage rejection ratio	See Note 2		70	100		80	100		dB
Supply current				8	11		8	11	mA
	Input offset voltage Average temperature coefficient of input offset voltage Input offset current‡ Input bias current‡ Common-mode input voltage range Maximum peak output voltage swing Large-signal differential voltage Input resistance Common-mode rejection ratio Supply-voltage rejection ratio		$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	PARAMETER CONDITIONS TA^{\dagger} MIN TYP MAX MIN Input offset voltage $V_{IC} = 0$, $R_S = 10 \text{ k}Ω$ Full range 13 13 Average temperature coefficient of input offset voltage $V_{IC} = 0$, $R_S = 10 \text{ k}Ω$ 18 18 Input offset current‡ $V_{IC} = 0$ 25°C 25 100 Input bias current‡ $V_{IC} = 0$ 25°C 50 200 Input bias current‡ $V_{IC} = 0$ 70°C 8 Common-mode input voltage range ±11 to ±11 Maximum peak output voltage swing $R_L = 10 \text{ k}Ω$ ±12 ±13.5 ±12 Large-signal differential voltage $V_O = \pm 10 \text{ V}$, $R_L = 2 \text{ k}Ω$ Full range 15 50 Input resistance $T_A = 25^{\circ}C$ 70 100 80 Supply-voltage rejection ratio See Note 2 70 100 80	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$

[†] Full range is 0°C to 70°C.

NOTE 2: Supply-voltage rejection ratio is measured for both supply magnitudes increasing or decreasing simultaneously.

operating characteristics, $V_{CC\pm}$ = $\pm 15~V$

PARAMETER		TEST CONDITIONS	MIN	TYP	MAX	UNIT
V _{O1} /V _{O2}	Crosstalk attentuation	f = 1 kHz		120		dB
SR	Slew rate		8	13		V/μs
B ₁	Unity-gain bandwidth			3		MHz
V _n	Equivalent input noise voltage	$f = 1 \text{ kHz}, R_S = 20 \Omega$		18		nV/√ Hz
In	Equivalent input noise current	f = 1 kHz		0.01		pA/√Hz



[‡] Input bias currents of a FET-input operational amplifier are normal junction reverse currents, which are temperature sensitive. Pulse techniques must be used that will maintain the junction temperatures as close to the ambient temperature as possible.

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